



## Product/Process Change Notice - PCN 20\_0288 Rev. -

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This notice is to inform you of a change that will be made to certain ADI products (see Appendix A) that you may have purchased in the last 2 years. **Any inquiries or requests with this PCN (additional data or samples) must be sent to ADI within 30 days of publication date.** ADI contact information is listed below.

**PCN Title:** Standby Mode Digital Functionality Change to ADXL362-MI

**Publication Date:** 21-Sep-2020

**Effectivity Date:** 21-Sep-2020 *(the earliest date that a customer could expect to receive changed material)*

**Revision Description:**

Initial Release.

**Description Of Change:**

This revision of the ASIC introduces a soft reset upon exiting Measure Mode. Upon writing 'OO' in the MEASURE register, configuration registers are preserved, however data registers and FIFO pointers are no longer valid. Data, FIFO, status registers, and interrupt register reads will only be supported in Measure mode.

The change is captured in the Product Data Sheet on page 13 with the following language "Note that data/FIFO reads and status register access are only supported in measurement mode."

**Reason For Change:**

The change is necessary to improve product robustness and quality.

**Impact of the change (positive or negative) on fit, form, function & reliability:**

The change only impacts the digital functionality associated with Standby mode. It does not impact the form, fit or function of the accelerometer as a whole.

**Product Identification** *(this section will describe how to identify the changed material)*

The ASIC revision will be printed on the label for traceability while we transition from rev. 2 to rev 4, beyond that customers need to keep track of what parts are being assembled in their products.

**Summary of Supporting Information:**

Qualification has been performed per Industry Standard Test Methods. See attached Qualification Results Summary. Data sheet (current version rev. SpB) is being revised and the changes are captured in rev. SpC.

**Comments**

Customers already using the ADXL362MI rev. 2 will have one year to transition to rev. 4. During this time we will support orders for both versions. Customers are advised to work with their customer service representative and to specify which ASIC revision is being requested. The revision will be printed on the label for traceability, beyond that customers need to keep track of what parts are being assembled in their products.

**Supporting Documents**

**Attachment 1: Type:** Qualification Results Summary

ADI\_PCN\_20\_0288\_Rev\_-\_ADXL362-MI ASIC AR3 Qualification Results .pdf

**For questions on this PCN, please send an email to the regional contacts below or contact your local ADI sales representatives.**

**Americas:**  
PCN\_Americas@analog.com

**Europe:**  
PCN\_Europe@analog.com

**Japan:**  
PCN\_Japan@analog.com

**Rest of Asia:**  
PCN\_ROA@analog.com

**Appendix A - Affected ADI Models**

**Added Parts On This Revision - Product Family / Model Number (2)**

ADXL362 / ADXL362BCCZ-MI-RL

ADXL362 / ADXL362BCCZ-MI-RL7

**Appendix B - Revision History**

<b>Rev</b>	<b>Publish Date</b>	<b>Effectivity Date</b>	<b>Rev Description</b>
Rev. -	21-Sep-2020	21-Sep-2020	Initial Release.

Analog Devices, Inc.

DocId:8289 Parent DocId:None Layout Rev:7

## ADXL362-MI ASIC Change

### Qualification Results Summary of the ADXL362-MI with ASIC AR3

<b>QUALIFICATION RESULTS</b>			
<b>TEST</b>	<b>SPECIFICATION</b>	<b>SAMPLE SIZE</b>	<b>RESULTS</b>
ESD – HBM	ESDA/JEDEC JS-001-2011	3 x 3 / voltage level	Pass ±1500V
ESD – FICDM	JESD22-C101	3 x 3 / voltage level	Pass ±1250V
Latch up	JESD78	3 x 3 / test	Pass ±200mA, +5.25V